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Inventor Name Search Result

Your Search was:

Last Name = SZCZEPANEK

First Name = ANDRE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60680592</u>	Not Issued	020	05/13/2005	RAPID I/O SYSTEMS	SZCZEPANEK, ANDRE
<u>60185802</u>	Not Issued	159	02/29/2000	COMMUNICATIONS INTERFACE BETWEEN CLOCK DOMAINS WITH MINIMAL LATENCY	SZCZEPANEK, ANDRE
<u>60104838</u>	Not Issued	159	10/19/1998	DUAL RING PORT	SZCZEPANEK, ANDRE
<u>60104837</u>	Not Issued	159	10/19/1998	MEDIA EXTENDER	SZCZEPANEK, ANDRE
<u>60104832</u>	Not Issued	159	10/19/1998	HEAD OF LINE BLOCKING	SZCZEPANEK, ANDRE
<u>10176215</u>	Not Issued	071	06/20/2002	COMMUNICATIONS SYSTEMS, APPARATUS AND METHODS	SZCZEPANEK, ANDRE
<u>09928188</u>	Not Issued	071	08/10/2001	NETWORK EXTENDER MODULE	SZCZEPANEK, ANDRE
<u>09755825</u>	Not Issued	041	01/05/2001	COMMUNICATIONS INTERFACE BETWEEN CLOCK DOMAINS WITH MINIMAL LATENCY	SZCZEPANEK, ANDRE
<u>09466038</u>	<u>6741611</u>	150	12/17/1999	PACKET MEMORY MANAGEMENT (PACMAN) SCHEME	SZCZEPANEK, ANDRE
<u>09465624</u>	Not Issued	161	12/17/1999	HIERARCHICAL RING TOPOLOGY USING GMII LEVEL SIGNALING	SZCZEPANEK, ANDRE
<u>09453486</u>	<u>6810044</u>	150	12/17/1999	IN ORDER BROADCAST MANAGEMENT (IOBMAN) SCHEME	SZCZEPANEK, ANDRE
<u>09410287</u>	<u>6690668</u>	150	09/30/1999	MODULAR INTERCONNECTION OF NETWORK SWITCHES	SZCZEPANEK, ANDRE

<u>09410206</u>	<u>6621818</u>	150	09/30/1999	RING CONFIGURATION FOR NETWORK SWITCHES	SZCZEPANEK, ANDRE
<u>08874801</u>	<u>5983305</u>	150	06/13/1997	NETWORK ADAPTER WITH DATA ALIGNER	SZCZEPANEK, ANDRE
<u>08859519</u>	<u>5832216</u>	150	05/20/1997	NETWORK ADAPTER HAVING SINGLE PORTED MEMORY WHICH IS ACCESSIBLE BY NETWORK AND PERIPHERAL BUS ON A TIME DIVISION MULTIPLEXED (TDM) BASIS	SZCZEPANEK, ANDRE
<u>08718148</u>	Not Issued	161	09/18/1996	IMPROVED COMMUNICATIONS SYSTEMS APPARATUS AND METHODS USING A SINGLE CHIP MULTISPEED MULTIPOINT NETWORK SWITCH	SZCZEPANEK, ANDRE
<u>08378892</u>	Not Issued	161	01/25/1995	MULTI-STATE CONTEXT SWITCH PROCESSING APPARATUS AND METHOD IMPLEMENTING DIRTY AND VALIDITY STATUS FLAGS	SZCZEPANEK, ANDRE
<u>08334511</u>	<u>5717932</u>	150	11/04/1994	DATA TRANSFER INTERRUPT PACING	SZCZEPANEK, ANDRE
<u>08334470</u>	<u>5784573</u>	150	11/04/1994	MULTI-PROTOCOL LOCAL AREA NETWORK CONTROLLER	SZCZEPANEK, ANDRE
<u>08334468</u>	Not Issued	161	11/04/1994	LAN ADAPTER WITH ADAPTIVE COMMIT LEVEL	SZCZEPANEK, ANDRE
<u>08334306</u>	Not Issued	166	11/04/1994	ASYNCHRONOUS DATA COMMUNICATION NETWORK ADAPTER HAVING MULTIPOINTED MEMORY	SZCZEPANEK, ANDRE
<u>08334299</u>	<u>5721841</u>	150	11/04/1994	ADAPTER HAVING DATA ALIGNER INCLUDING REGISTER BEING LOADED TO OR FROM MEMORY WITH AN OFFSET IN ACCORDANCE WITH PREDETERMINED NETWORKS FRAGMENTATION PARAMETERS	SZCZEPANEK, ANDRE
<u>08298814</u>	Not	166	08/31/1994	LOCAL AREA NETWORK	SZCZEPANEK,

	Issued			ADAPTER WITH MAIN PROCESSOR AND AUXILIARY PROCESSOR	ANDRE
<u>08150341</u>	Not Issued	166	11/10/1993	MULTI-STATE CONTEXT SWITCH PROCESSING APPARATUS AND METHOD IMPLEMENTING DIRTY AND VALIDITY STATUS FLAGS	SZCZEPANEK, ANDRE
<u>08145040</u>	<u>5374926</u>	150	10/27/1993	COMMUNICATIONS CIRCUIT HAVING AN INTERFACE FOR EXTERNAL ADDRESS DECODING	SZCZEPANEK, ANDRE
<u>08061643</u>	<u>5497107</u>	150	05/13/1993	MULTIPLE, SELECTABLE PLAS HAVING SHARED INPUTS AND OUTPUTS	SZCZEPANEK, ANDRE
<u>08061602</u>	<u>5517638</u>	150	05/13/1993	DYNAMIC CLOCK SWITCHING CIRCUITRY AND METHOD	SZCZEPANEK, ANDRE
<u>08024893</u>	<u>5404450</u>	150	03/01/1993	COMMUNICATIONS PROCESSOR SYSTEM WITH CONTROL OF DOWNLOADED TASKS	SZCZEPANEK, ANDRE
<u>07879090</u>	Not Issued	161	05/01/1992	DATA PRIORITIZING SYSTEM	SZCZEPANEK, ANDRE
<u>07874250</u>	<u>5299193</u>	150	04/24/1992	SIGNAL INTERFACE FOR COUPLING A NETWORK FRONT END CIRCUIT TO A NETWORK ADAPTER CIRCUIT	SZCZEPANEK, ANDRE
<u>07874242</u>	<u>5305317</u>	150	04/24/1992	LOCAL AREA NETWORK ADAPTIVE CIRCUIT FOR MULTIPLE NETWORK TYPES	SZCZEPANEK, ANDRE
<u>07873916</u>	Not Issued	166	04/24/1992	INTERFACE DEVICE FOR COUPLING A HOST DEVICE HAVING A NETWORK INTERFACE TO A COMPUTER NETWORK HAVING A PREDETERMINED COMMUNICATIONS MEDIUM AND A PREDETERMINED COMMUNICATIONS PHYSICAL LAYER	SZCZEPANEK, ANDRE
<u>07759981</u>	Not Issued	166	09/16/1991	COMMUNICATIONS CIRCUIT HAVING AN INTERFACE FOR EXTERNAL ADDRESS	SZCZEPANEK, ANDRE

				DECODING	
<u>07446505</u>	<u>5265228</u>	150	12/05/1989	APPARATUS FOR TRANSFER OF DATA UNITS BETWEEN BUSES	SZCZEPANEK, ANDRE
<u>07446352</u>	Not Issued	166	12/05/1989	DATA PRIORITIZING SYSTEM	SZCZEPANEK, ANDRE'
<u>07446019</u>	<u>5715419</u>	150	12/05/1989	DATA COMMUNICATIONS SYSTEM WITH ADDRESS REMAPPING FOR EXPANDED MEMORY ACCESS	SZCZEPANEK, ANDRE
<u>07445562</u>	Not Issued	166	12/05/1989	DIGITAL PROCESSOR	SZCZEPANEK, ANDRE'
<u>07445066</u>	Not Issued	166	12/04/1989	COMMUNICATIONS PROCESSOR SYSTEM WITH CONTROL OF DOWNLOADED TASKS	SZCZEPANEK, ANDRE
<u>07417997</u>	<u>5113418</u>	150	10/06/1989	ELASTIC BUFFER FOR LOCAL AREA NETWORK	SZCZEPANEK, ANDRE
<u>07405361</u>	Not Issued	166	09/11/1989	COMMUNICATIONS CIRCUIT HAVING AN INTERFACE FOR EXTERNAL ADDRESS DECODING	SZCZEPANEK, ANDRE
<u>07348107</u>	<u>4933662</u>	150	05/01/1989	A COMPARATOR HAVING VARIABLE WIDTH COMPARISONS	SZCZEPANEK, ANDRE
<u>07259492</u>	Not Issued	166	10/17/1988	COMPARATOR HAVING VARIABLE WIDTH COMPARISONS	SZCZEPANEK, ANDRE
<u>07064289</u>	<u>4866421</u>	150	06/18/1987	COMMUNICATIONS CIRCUIT HAVING AN INTERFACE FOR EXTERNAL ADDRESS DECODING	SZCZEPANEK, ANDRE
<u>06926433</u>	<u>4959811</u>	150	11/03/1986	A CONTENT ADDRESSABLE MEMORY INCLUDING COMPARISON INHIBIT AND SHIFT REGISTER CIRCUITS	SZCZEPANEK, ANDRE
<u>06869485</u>	Not Issued	161	06/02/1986	HIGH SPEED PROGRAMMABLE LOGIC ARRAY	SZCZEPANEK, ANDRE
<u>06830159</u>	Not Issued	166	02/18/1986	A COMPARATOR HAVING VARIABLE WIDTH COMPARISONS	SZCZEPANEK, ANDRE
<u>06809626</u>	<u>4674086</u>	150	12/16/1985	TOKEN RING ACCESS CONTROL PROTOCOL CIRCUIT	SZCZEPANEK, ANDRE

<u>06809625</u>	<u>4736368</u>	150	12/16/1985	PRIORITY TOKEN PROTOCOL CIRCUIT FOR A TOKEN RING LOCAL AREA NETWORK ADAPTOR	SZCZEPANEK, ANDRE
<u>06808149</u>	<u>4884286</u>	150	12/12/1985	ELASTIC BUFFER FOR LOCAL AREA NETWORKS	SZCZEPANEK, ANDRE
<u>06440380</u>	<u>4698753</u>	150	11/09/1982	MULTIPROCESSOR INTERFACE DEVICE	SZCZEPANEK, ANDRE

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